Electronic Acknowledgement Receipt			
EFS ID:	8474097		
Application Number:	10620045		
International Application Number:			
Confirmation Number:	7529		
Title of Invention:	METHOD AND APPARATUS FOR AUTOMATIC GENERATION OF MULTIPLE INTEGRATED CIRCUIT SIMULATION CONFIGURATION		
First Named Inventor/Applicant Name:	Rajarshi Bhattacharya		
Correspondence Address:	Ryan, Mason & Lewis, LLP - 90 Forest Avenue - Locust Valley NY 11560 US 5167597517 -		
Filer:	Joseph Bernard Ryan		
Filer Authorized By:			
Attorney Docket Number:	1-4-2-2-1		
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Application Type:	Utility under 35 USC 111(a)		
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Documen Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1 Issue Fee Payment (PTO-85B)	1150-1145_IF.pdf	167615	no	1	
		d4b1ac216c39054acd34c3fb0d5el246018e 5e79			

Warnings:

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2	Fee Worksheet (PTO-875)	fee-info.pdf	31886	no	2
			25202b		

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